

Docket No.: P2002,0587

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : THOMAS HANUSCHEK ET AL.

Filed : CONCURRENTLY HEREWITH

Title : INTEGRATED MICROCONTROLLER MODULE AND METHOD
FOR CHECKING THE FUNCTIONALITY OF AN INTEGRATED
MEMORY OF THE MICROCONTROLLER MODULE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

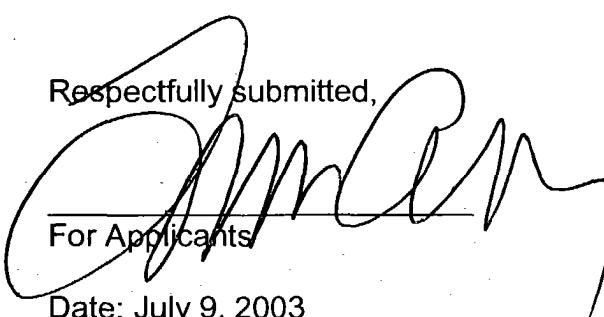
In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications
are submitted herewith:

U.S. Patent No. 6,000,048 (Krishna et al.), dated December 7, 1999.

Respectfully submitted,

For Applicants

Date: July 9, 2003


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FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(37 CFR 1.98(b))Attorney Docket No.: P2002,0587
Appl. No.: _____Applicant: THOMAS HANUSCHEK ET
AL.Filing Date: July 9, 2003
Group Art Unit: _____

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	6,000,048	12/7/99	Krishna et al.			
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J						
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

EXAMINER		DATE CONSIDERED